NOTE: Routing for BACK_CLK125 is 'serial' not 'star' as shown here.
External 25 MHz Oscillator

Copper Discharge Strips

- **R13**: GND
- **EN**: GND
- **22**: GND
- **VCC**: GND
- **CLK_OUT**: GND
- **VDD**: GND

**OSCILLATORS, DISCHARGE, & LEDS**

**MISC. LOGIC**

**REV**: A4
**DRAWN**: C.Harabedian
**CHECKED**: KOTO Group
**QUALITY CONTROL**: <QC By>
**RELEASED**: <Released By>
**DRAWING NO**: 001
**COMPANY**: Univ. of Michigan - Physics

**FILENANE**: FANOUT_A5.sch
**SAVED**: 8/3/2010
**SHEET**: 12

**SCALE**: 1:1
**SIZE**: B

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</tr>
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**FILENANE**: FANOUT_A5.sch
**SAVED**: 8/3/2010
**SHEET**: 12

**SCALE**: 1:1
**SIZE**: B
These caps are for all power pins on pages:
3-8, oscillator on page 9,
all power pins for the FPGA,
the TI_SN74BCT60EP on page 17
and
U95 on page 17,
103 cap. for 3.3VDC

+3.3VDC
+2.5VDC
+1.2VDC
+5.0VDC
GND